

SILICON POWER TRANSISTOR 2SC2335

NPN SILICON TRIPLE DIFFUSED TRANSISTOR FOR HIGH-SPEED HIGH-VOLTAGE SWITCHING

The 2SC2335 is a mold power transistor developed for high-speed high-voltage switching, and is ideal for use as a driver in devices such as switching regulators, DC/DC converters, and high-frequency power amplifiers.

FEATURES

- Low collector saturation voltage: VcE(sat) = 1.0 V MAX. @ Ic = 3.0 A
- Fast switching speed: $t_f = 1.0 \mu s$ MAX. @Ic = 3.0 A
- Wide base reverse-bias SOA: VCEX(SUS)1 = 450 V MIN. @Ic = 3.0 A

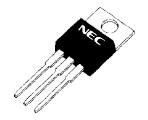
ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

Parameter	Symbol	Conditions	Ratings	Unit
Collector to base voltage	Vсво		500	٧
Collector to emitter voltage	VCEO		400	V
Emitter to base voltage	VEBO		7.0	V
Collector current (DC)	C(DC)		7.0	Α
Collector current (pulse)	C(pulse)	PW ≤ 300 <i>μ</i> s,	15	Α
		duty cycle ≤ 10%		
Base current (DC)	B(DC)		3.5	Α
Total power dissipation	Р⊤	Tc = 25°C	40	W
		T _A = 25°C	1.5	W
Junction temperature	Tj		150	°C
Storage temperature	Tstg		-55 to +150	°C

ORDERING INFORMATION

Part No.	Package
2SC2335	TO-220AB

(TO-220AB)



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ELECTRICAL CHARACTERISTICS (TA = 25°C)

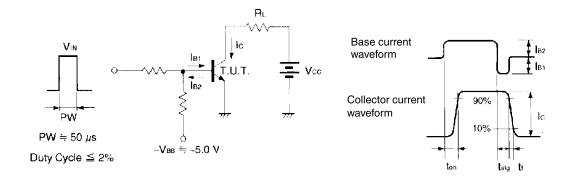
Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Collector to emitter voltage	VCEO(SUS)	Ic = 3.0 A, Iв1 = 0.6 A, L = 1 mH	400			V
Collector to emitter voltage	VCEX(SUS)1	Ic = 3.0 A, I _{B1} = $-I_{B2}$ = 0.6 A, V _{BE(OFF)} = -5.0 V, L = 180 μ H, clamped	450			>
Collector to emitter voltage	VCEX(SUS)2	$I_{C} = 6.0 \text{ A}, I_{B1} = 2.0 \text{ A}, -I_{B2} = 0.6 \text{ A},$ $V_{BE(OFF)} = -5.0 \text{ V}, L = 180 \ \mu\text{H}, clamped}$	400			٧
Collector cutoff current	СВО	Vcb = 400 V, IE = 0 A			10	μΑ
Collector cutoff current	CER	$V_{CE} = 400 \text{ V}, \text{ Rbe} = 51 \Omega, \text{ Ta} = 125^{\circ}\text{C}$			1.0	mA
Collector cutoff current	ICEX1	$V_{CE} = 400 \text{ V}, V_{BE(OFF)} = -1.5 \text{ V}$			10	μΑ
Collector cutoff current	CEX2	$V_{CE} = 400 \text{ V}, V_{BE(OFF)} = -1.5 \text{ V},$ $T_A = 125^{\circ}\text{C}$			1.0	mA
Emitter cutoff current	ЕВО	V _{EB} = 5.0 V, I _C = 0 A			10	μΑ
DC current gain	h _{FE1}	$V_{CE} = 5.0 \text{ V}, I_{C} = 0.1 \text{ A}^{Note}$	20		80	
DC current gain	h _{FE2}	$V_{CE} = 5.0 \text{ V}, I_{C} = 1.0 \text{ A}^{Note}$	20		80	
DC current gain	h _{FE3}	$V_{CE} = 5.0 \text{ V}, I_{C} = 3.0 \text{ A}^{Note}$	10			
Collector saturation voltage	V _{CE(sat)}	$I_{C} = 3.0 \text{ A}, I_{B} = 0.6 \text{ A}^{\text{Note}}$			1.0	٧
Base saturation voltage	V _{BE(sat)}	$I_{C} = 3.0 \text{ A}, I_{B} = 0.6 \text{ A}^{\text{Note}}$		-	1.2	٧
Turn-on time	ton	Ic = 3.0 A, $R_L = 50 \Omega$,			1.0	μs
Storage time	tstg	$I_{B1} = -I_{B2} = 0.6 \text{ A}, \text{ Vcc} \cong 150 \text{ V}$			2.5	μs
Fall time	tf	Refer to the test circuit.			1.0	μs

Note Pulse test PW \leq 350 μ s, duty cycle \leq 2%

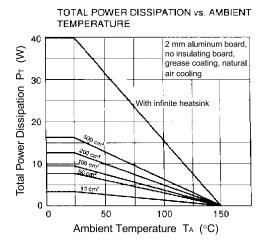
hfe CLASSIFICATION

Marking	М	L	К	
h _{FE2}	20 to 40	30 to 60	40 to 80	

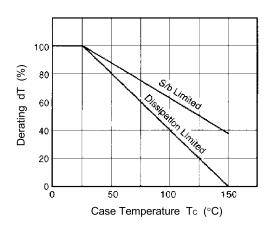
SWITCHING TIME (ton, tstg, tf) TEST CIRCUIT



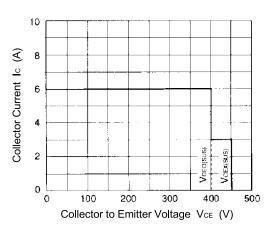
TYPICAL CHARACTERISTICS (TA = 25°C)



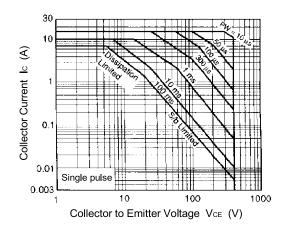
DERATING CURVE OF SAFE OPERATING AREAS



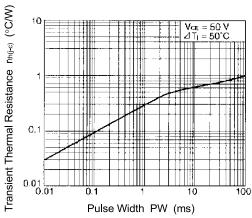
REVERSE BIAS SAFE OPERATING AREAS



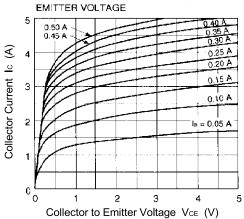
FORWARD BIAS SAFE OPERATING AREAS



TRANSIENT THERMAL RESISTANCE

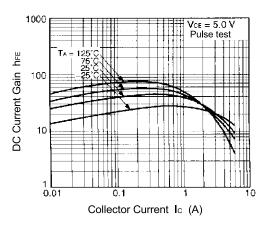


COLLECTOR CURRENT vs. COLLECTOR TO



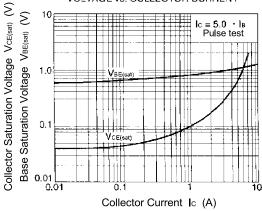
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DC CURRENT GAIN vs. COLLECTOR CURRENT

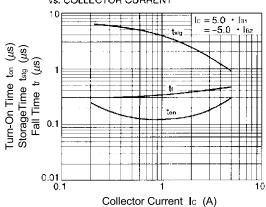


VOLTAGE vs. COLLECTOR CURRENT

BASE AND COLLECTOR SATURATION



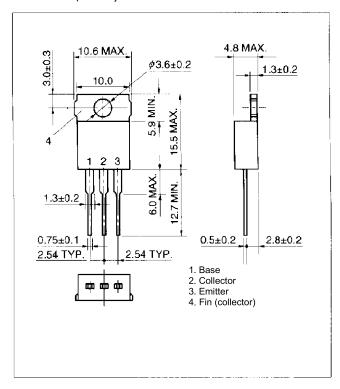
TURN ON TIME, STORAGE TIME AND FALL TIME vs. COLLECTOR CURRENT





PACKAGE DRAWING (UNIT: mm)

TO-220AB (MP-25)



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