Silicon NPN Epitaxial

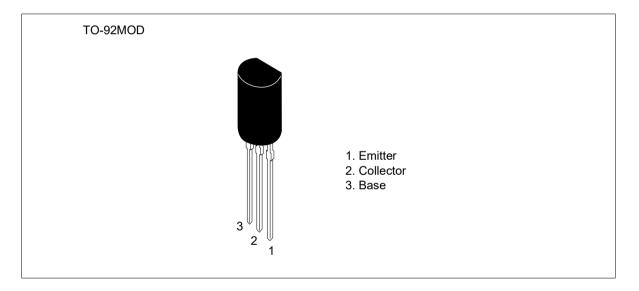
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ADE-208-1137 (Z) 1st. Edition Mar. 2001

Application

- Low frequency power amplifier
- Complementary pair with 2SB647/A

Outline





Absolute Maximum Ratings (Ta = 25°C)

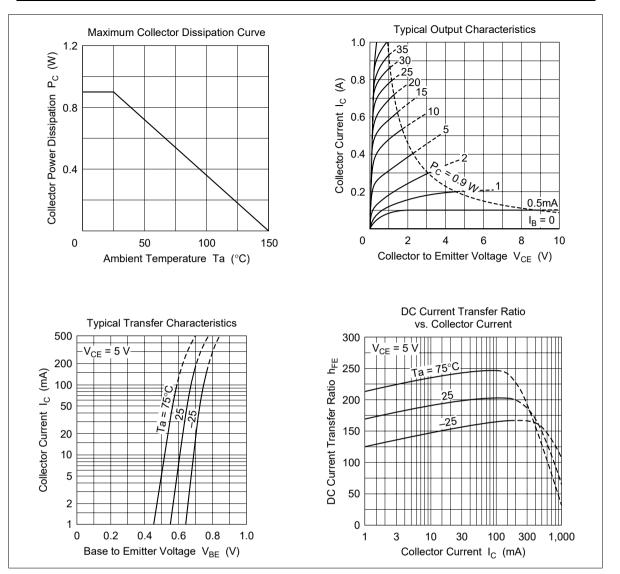
Item	Symbol	2SD667	2SD667A	Unit
Collector to base voltage	V _{CBO}	120	120	V
Collector to emitter voltage	V _{CEO}	80	100	V
Emitter to base voltage	V _{EBO}	5	5	V
Collector current	Ι _c	1	1	А
Collector peak current	İ _{C(peak)}	2	2	А
Collector power dissipation	Pc	0.9	0.9	W
Junction temperature	Tj	150	150	°C
Storage temperature	Tstg	-55 to +150	-50 to +150	°C

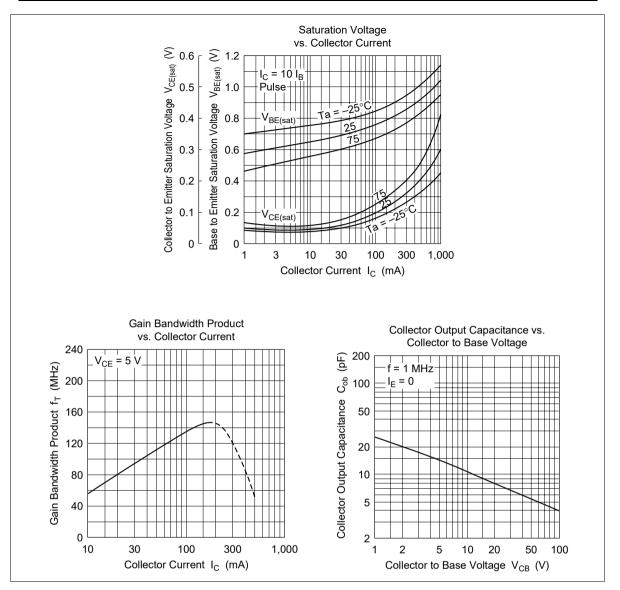
Electrical Characteristics (Ta = 25°C)

		2SD6	67		2SD667A				
Item	Symbol	Min	Тур	Мах	Min	Тур	Мах	Unit	Test conditions
Collector to base breakdown voltage	$V_{\rm (BR)CBO}$	120	_	_	120			V	$I_{c} = 10 \ \mu A, I_{E} = 0$
Collector to emitter breakdown voltage	$V_{\rm (BR)CEO}$	80	—	—	100	—	—	V	I_c = 1 mA, R_{BE} = ∞
Emitter to base breakdown voltage	$V_{\rm (BR)EBO}$	5	—	—	5	—	—	V	$I_{\rm E} = 10 \ \mu A, \ I_{\rm C} = 0$
Collector cutoff current	I _{CBO}	_		10	_		10	μA	V _{CB} = 100 V, I _E = 0
DC current transfer ratio	h_{FE1}^{*1}	60	—	320	60	—	200		V _{CE} = 5 V, I _c = 150 mA* ²
	h_{FE2}	30	—	—	30	—	—		V _{CE} = 5 V, I _c = 500 mA* ²
Collector to emitter saturation voltage	$V_{\text{CE(sat)}}$	_	—	1	_		1	V	I _c = 500 mA, I _B = 50 mA* ²
Base to emitter voltage	V_{BE}	—	—	1.5	—	—	1.5	V	V _{CE} = 5 V, I _c = 150 mA* ²
Gain bandwidth product	f _T	—	140	—	—	140	—	MHz	V _{CE} = 5 V, I _c = 150 mA* ²
Collector output capacitance	Cob	_	12	—	—	12		pF	$V_{_{CB}} = 10 \text{ V}, \text{ I}_{_{E}} = 0,$ f = 1 MHz
Notes: 1. The 2SD667 2. Pulse test	and 2SD6	667A a	re grou	ped by	h _{FE1} as	follows.			
2. 1 dice toot	•		-						

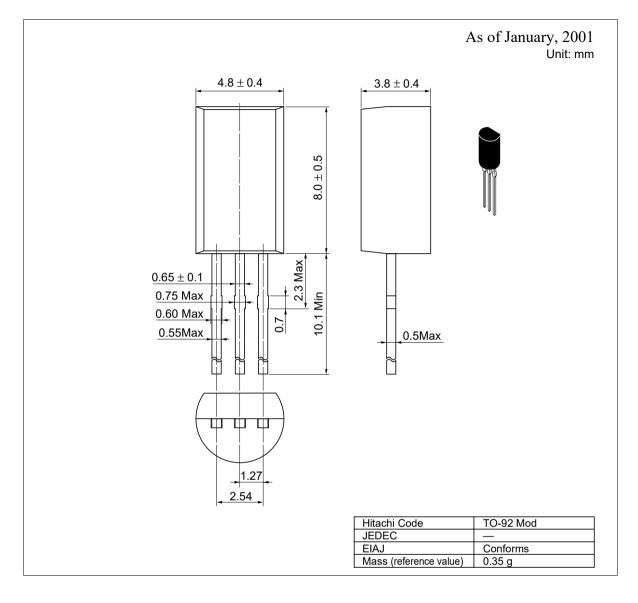
	В	С	D
2SD667	60 to 120	100 to 200	160 to 320
2SD667A	60 to 120	100 to 200	

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Package Dimensions



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