

QUADRUPLE 3-STATE BUFFERS OE LOW

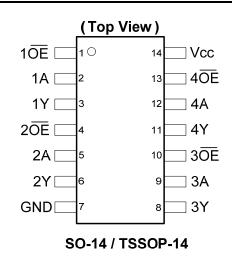
Description

The 74HC125 provides provides four independent buffer gates with 3-state outputs. Each buffer has a separate enable pin that if driven with a high logic level places the corresponding output in the high impedance state. The device is designed for operation with a power supply range of 2.0V to 6.0V.

Features

- Wide Supply Voltage Range from 2.0V to 6.0V
- Sinks or sources 4mA at V_{CC} = 4.5V
- CMOS low power consumption
- Schmitt Trigger Action at All Inputs
- ESD Protection Exceeds JESD 22
 - 200-V Machine Model (A115-A) .
 - 2000-V Human Body Model (A114-A) .
 - Exceeds 1000-V Charged Device Model (C101C)
- Range of Package Options SO-14 and TSSOP-14
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

Pin Assignments



Applications

- General Purpose Logic
- Wide array of products such as:
 - PCs, Networking, Notebooks, Netbooks
 - Computer Peripherals, Hard Drives, CD/DVD ROM
 - TV, DVD, DVR, Set Top Box

Notes:

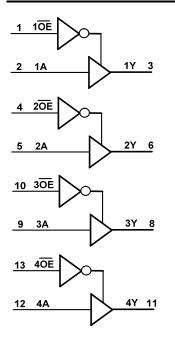
- No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
 See http://www.diodes.com for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.



Pin Descriptions

Pin Number	Pin Name	Function
1	1 0E	Data Enable Input (active low)
2	1A	Data Input
3	1Y	Data Output
4	20E	Data Enable Input (active low)
5	2A	Data Input
6	2Y	Data Output
7	GND	Ground
8	3Y	Data Output
9	3A	Data Input
10	30E	Data Enable Input (active low)
11	4Y	Data Outp
12	4A	Data Input
13	40E	Data Enable Input (active low)
14	V _{CC}	Supply Voltage

Logic Diagram



Function Table

Inp	Output	
ŌE	Α	Y
L	Н	Н
L	L	L
Н	Х	Z



Absolute Maximum Ratings (Note 4) (@T_A = +25°C, unless otherwise specified.)

Symbol	Description	Rating	Unit
ESD HBM	Human Body Model ESD Protection	2	KV
ESD CDM	Charged Device Model ESD Protection	1	KV
ESD MM	Machine Model ESD Protection	200	V
Vcc	Supply Voltage Range	-0.5 to +7.0	V
VI	Input Voltage Range note 3)	-0.5 to +7.0	V
I _{IK}	Input Clamp Current $V_{I} < -0.5V$ or $V_{I} > V_{CC} + 0.5V$	±20	mA
loĸ	Output Clamp Current $V_O < -0.5V$ or $V_O > V_{CC} + 0.5V$	±20	mA
lo	Continuous Output Current -0.5V < V _O V _{CC} +0.5V	+/- 25	mA
Icc	Continuous Current Through V _{CC}	50	mA
I _{GND}	Continuous Current Through GND	-50	mA
TJ	Operating Junction Temperature	-40 to +150	°C
T _{STG}	Storage Temperature	-65 to +150	°C
PTOT	Total Power Dissipation	500	mW

Notes: 4. Stresses beyond the absolute maximum may result in immediate failure or reduced reliability. These are stress values and device operation should be within recommend values.

5. Input Voltage cannot exceed V_{CC} to the extent the Maximum clamp current is exceeded.

Recommended Operating Conditions (Note 6) (@T_A = +25°C, unless otherwise specified.)

Symbol	Parameter	Conditions	Min	Max	Unit
V _{CC}	Supply Voltage		2.0	6.0	V
VI	Input Voltage		0	Vcc	V
Vo	Output Voltage		0	Vcc	V
	V _{CC} = 2.0V		625		
Δt/ΔV	Input Transition Rise or Fall Rate	V _{CC} = 4.5V		140	ns/V
		V _{CC} = 6.0V		85	
T _A	Operating Free-Air Temperature		-40	+125	°C

Note: 6. Unused inputs should be held at V_{CC} or Ground.



Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Cumb al	Parameter Te	Test Conditions	V	T _A = -40°	C to +85°C	T _A = -40°C	to +125°C	l Init
Symbol Parameter	Parameter	Parameter Test Conditions	Vcc	Min	Max	Min	Max	Unit
			2.0V	1.5		1.5		
VIH	High-level Input Voltage		4.5V	3.15		3.15		V
	Voltage		6.0V	4.2		4.2		
			2.0V		0.5		0.5	
VIL	Low-level Input voltage		4.5V		1.35		1.35	V
	vonago		6.0V		1.8		1.8	
		I _{OH} = -20μA	2.0V	1.9		1.9		
		I _{OH} = -20μA	4.5V	4.4		4.4		V
V _{OH} High-level Output Voltage		I _{OH} = -20µА	6.0V	5.9		5.9		
	Voltage	I _{OH} = -4.0mA	4.5V	3.84		3.7		
	I _{OH} = -5.2mA	6.0V	5.34		5.2		-	
		I _{OL} = 20μA	2.0V		0.1		0.1	
		I _{OL} = 20μA	4.5V		0.1		0.1	
V _{OL}	Low-level Output Voltage	I _{OL} = 20μA	6.0V		0.1		0.1	V
	Voltage	I _{OL} = 4mA	4.5V		0.33		0.44	
	I _{OL} = 5.2mA	6.0V		0.33		0.44		
I _{OZ}	Z State Leakage Current	$V_0 = 0$ to 6.0V $V_1 = GND$ or 6.0V	6.0V		± 5.0		± 10	μA
I _I	Input Current	V _I = GND to 5.5V	6.0V		± 1		± 1	μA
Icc	Supply Current	$V_I = GND \text{ or } V_{CC}, I_O = 0$	6.0V		20		40	μA

Switching Characteristics

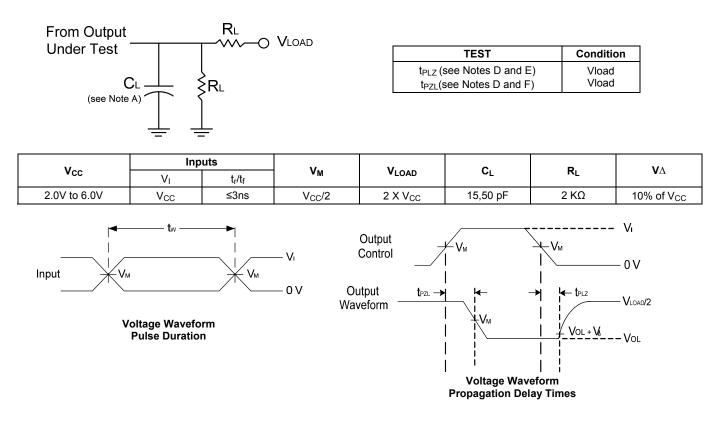
Symbol Parameter	Test	N/		T _A = +25°C	:	-40°C to +85°C	-40°C to +125°C	Unit	
Symbol	Parameter	Conditions	Vcc	Min	Тур.	Max	Max	Max	Unit
	Description	Figure 1	2.0V	_	30	100	125	150	
t _{PD}	Propagation Delay A _N to Y _N	Figure 1 C _L = 50 pF	4.5V	_	11	20	25	30	ns
	Delay AN IO IN	CL = 50 pF	6.0V	—	9	17	21	26	
$t_{EN} = \frac{Enable Time}{OE_N to Y_N}$	O = FO = F	2.0V	—	41	125	155	190		
		4.5V	_	15	25	31	38	ns	
		6.0V	_	12	21	26	32		
		Eisense 4	2.0V	_	41	125	155	190	
t _{DIS}		$C_1 = 50 \text{ pF}$	4.5V	—	15	25	31	38	ns
OE to Y _N	DE to Y_N $C_L = 50 \text{ pF}$	6.0V	—	12	21	26	32		
t _t Transition time	F : 4	Figure 1	2.0V	_	14	60	75	90	
	Transition time	Figure 1	4.5V	_	5	12	15	18	ns
		C _L = 50 pF	6.0V	_	4	10	13	15	

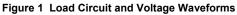
Operating Characteristics (@T_A = +25°C, unless otherwise specified.)

Parameter		Test Conditions	V _{CC} = 6V Typ	Unit
C _{pd}	Power Dissipation Capacitance per Gate	f = 1MHz	22	pF
CI	Input Capacitance	$V_I = V_{CC} - or GND$	4	pF



Parameter Measurement Information





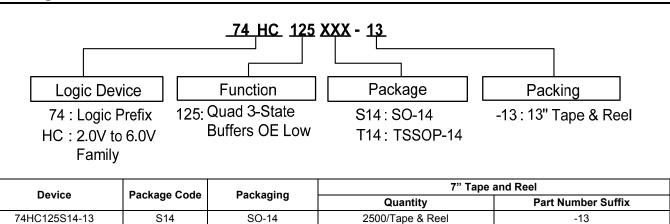
Notes: A. Includes test lead and test apparatus capacitance.

- B. All pulses are supplied at pulse repetition rate \leq 1 MHz.
- C. The inputs are measured one at a time with one transition per measurement.
- D. For the 3 state device t_{PLZ} and t_{PZL} are the same as $t_{\text{PD}}.$
- E. t_{PZL} is measured at V_M.
- D. t_{PLZ} is measured at V_{OL} +V_{Δ}.



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Ordering Information



2500/Tape & Reel

TSSOP-14

T14

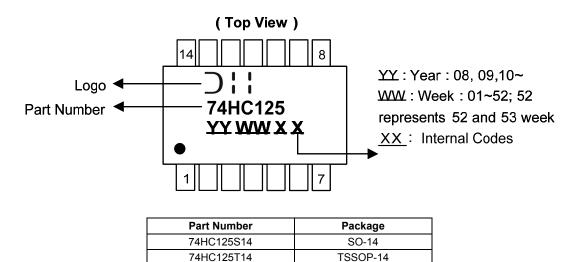
Marking Information

74HC125T14-13

(1) SO-14, TSSOP-14

Pb,

Pb

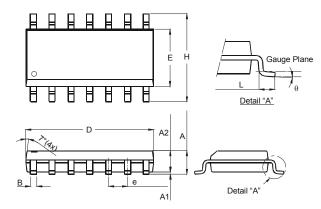




Package Outline Dimensions (All dimensions in mm.)

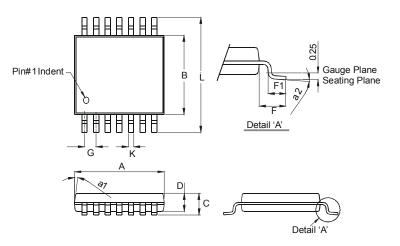
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.

Package Type: SO-14



	SO-14						
Dim	Min	Max					
Α	1.47	1.73					
A1	0.10	0.25					
A2	1.45 Typ						
В	0.33	0.51					
D	8.53	8.74					
E	3.80	3.99					
е	1.27	Тур					
н	5.80	6.20					
L	0.38	1.27					
θ	0°	8°					
All Di	mensions	s in mm					

Package Type: TSSOP-14



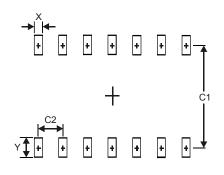
	TSSOP-1	TSSOP-14					
Dim	Min Max						
a1	7° (4X)					
a2	0°	8°					
Α	4.9	5.10					
В	4.30 4.50						
С	_	1.2					
D	0.8	1.05					
F	1.00	Тур					
F1	0.45 0.75						
G	0.65	Тур					
κ	0.19 0.30						
L	L 6.40 Typ						
All Dir	nensions	s in mm					



Suggested Pad Layout

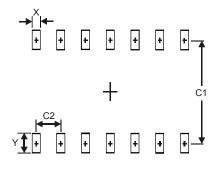
Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for latest version.

Package Type: SO-14



Dimensions	Value (in mm)
Х	0.60
Y	1.50
C1	5.4
C2	1.27

Package Type: TSSOP-14



Dimensions	Value (in mm)
Х	0.45
Y	1.45
C1	5.9
C2	0.65



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