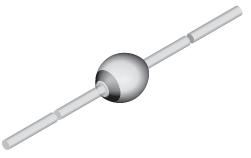
BYT51A, BYT51B, BYT51D, BYT51G, BYT51J, BYT51K, BYT51M



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**Vishay Semiconductors** 

# **Standard Avalanche Sinterglass Diode**



949539

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### **DESIGN SUPPORT TOOLS**



### **MECHANICAL DATA**

Case: SOD-57 Terminals: plated axial leads, solderable per MIL-STD-750, method 2026 Polarity: color band denotes cathode end Mounting position: any Weight: approx. 369 mg

#### **FEATURES**

- Glass passivated junction
- · Hermetically sealed package
- Low reverse current
- AEC-Q101 gualified
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

#### **APPLICATIONS**

Rectification diode



ORDERING INFORMATION (Example)				
DEVICE NAME	ORDERING CODE	TAPED UNITS	MINIMUM ORDER QUANTITY	
BYT51M	BYT51M-TR	5000 per 10" tape and reel	25 000	
BYT51M	BYT51M-TAP	5000 per ammopack	25 000	

PARTS TABLE		
PART	TYPE DIFFERENTIATION	PACKAGE
BYT51A	$V_{R} = 50 \text{ V}; \text{ I}_{F(AV)} = 1.5 \text{ A}$	SOD-57
BYT51B	$V_{R} = 100 \text{ V}; I_{F(AV)} = 1.5 \text{ A}$	SOD-57
BYT51D	$V_{R} = 200 \text{ V}; I_{F(AV)} = 1.5 \text{ A}$	SOD-57
BYT51G	$V_R = 400 \text{ V}; I_{F(AV)} = 1.5 \text{ A}$	SOD-57
BYT51J	$V_{R} = 600 \text{ V}; I_{F(AV)} = 1.5 \text{ A}$	SOD-57
BYT51K	$V_{R} = 800 \text{ V}; I_{F(AV)} = 1.5 \text{ A}$	SOD-57
BYT51M	$V_{R} = 1000 \text{ V}; \text{ I}_{F(AV)} = 1.5 \text{ A}$	SOD-57

BYT51A, BYT51B, BYT51D, BYT51G, BYT51J, BYT51K, BYT51M



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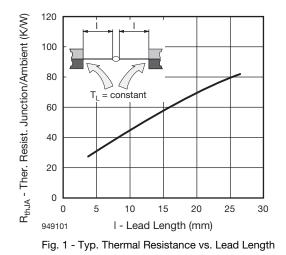
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<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	PART	SYMBOL	VALUE	UNIT	
	See electrical characteristics	BYT51A	$V_{R} = V_{RRM}$	50	V	
		BYT51B	$V_{R} = V_{RRM}$	100	V	
		BYT51D	$V_{R} = V_{RRM}$	200	V	
Reverse voltage = repetitive peak reverse voltage		BYT51G	$V_{\rm R} = V_{\rm RRM}$ 400		V	
		BYT51J	$V_{R} = V_{RRM}$	600	V	
		BYT51K	$V_{R} = V_{RRM}$	800	V	
		BYT51M	$V_R = V_{RRM}$	1000	V	
Peak forward surge current	t <sub>p</sub> = 10 ms, half sine wave		I <sub>FSM</sub>	50	А	
Repetitive peak forward current			I <sub>FRM</sub>	9	А	
Assessed as a factor of a summark	l = 10 mm		I <sub>F(AV)</sub>	1.5	А	
Average forward current	On PC board		I <sub>F(AV)</sub>	1	А	
Junction and storage temperature range			$T_j = T_{stg}$	-55 to +175	°C	
Non repetitive reverse avalanche energy	l(BR)R = 1 A		ER	20	mJ	

MAXIMUM THERMAL RESISTANCE (T <sub>amb</sub> = 25 °C, unless otherwise specified)					
PARAMETER TEST CONDITION		SYMBOL	VALUE	UNIT	
Junction ambient	Lead length I = 10 mm, $T_L$ = constant	R <sub>thJA</sub>	45	K/W	
Junction ambient	On PC board with spacing 25 mm	R <sub>thJA</sub>	100	K/W	

<b>ELECTRICAL CHARACTERISTICS</b> ( $T_{amb}$ = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Forward voltage	I <sub>F</sub> = 1 A	V <sub>F</sub>	-	0.95	1.1	V
Torward voltage	I <sub>F</sub> = 1 A, T <sub>j</sub> = 175 °C	V <sub>F</sub>	-	-	1	V
Beverse current	$V_{R} = V_{RRM}$	I <sub>R</sub>	-	-	1	μA
neverse current	$V_R = V_{RRM}, T_j = 150 \ ^\circ C$	I <sub>R</sub>	-	-	100	μA
Reverse recovery time	$I_F = 0.5 \text{ A}, I_R = 1 \text{ A}, I_R = 0.25 \text{ A}$	t <sub>rr</sub>	-	-	4	μs

## TYPICAL CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)



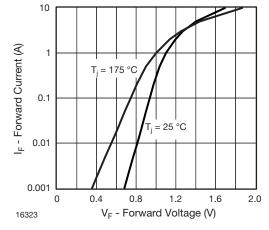


Fig. 2 - Forward Current vs. Forward Voltage

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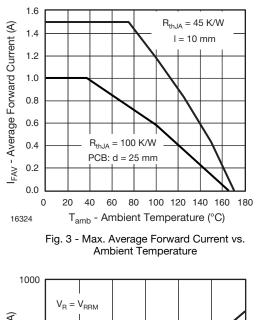
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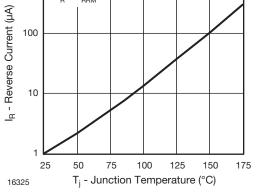


Fig. 4 - Reverse Current vs. Junction Temperature

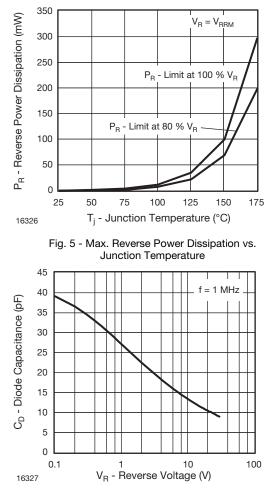
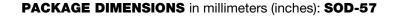
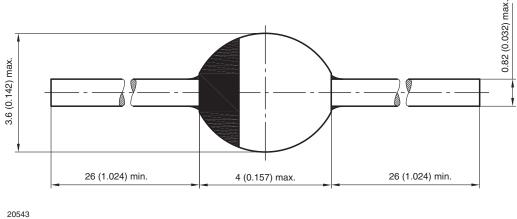


Fig. 6 - Diode Capacitance vs. Reverse Voltage





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