J309, J310

Preferred Device

JFET VHF/UHF Amplifiers

N-Channel — Depletion

Features

• Pb-Free Packages are Available*

MAXIMUM RATINGS

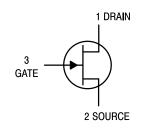
Rating	Symbol	Value	Unit
Drain-Source Voltage	V _{DS}	25	Vdc
Gate-Source Voltage	V_{GS}	25	Vdc
Forward Gate Current	I _{GF}	10	mAdc
Total Device Dissipation @ T _A = 25°C Derate above = 25°C	P _D	350 2.8	mW mW/°C
Junction Temperature Range	TJ	-65 to +125	°C
Storage Temperature Range	T _{stg}	-65 to +150	°C

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.



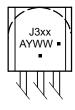
ON Semiconductor®

http://onsemi.com





MARKING DIAGRAM



J3xx = Device Code

xx = 09 or 10

A = Assembly Location

Y = Year WW = Work Week

■ = Pb-Free Package

(Note: Microdot may be in either location)

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 3 of this data sheet.

Preferred devices are recommended choices for future use and best overall value.

^{*}For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

J309, J310

ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise noted)

Characteristic		Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS			<u> </u>			I
Gate – Source Breakdown Voltage (I _G = −1.0 μAdc, V _{DS} = 0)		V _{(BR)GSS}	-25	_	-	Vdc
Gate Reverse Current $(V_{GS} = -15 \text{ Vdc}, V_{DS} = 0, T_A = 25^{\circ}\text{C})$ $(V_{GS} = -15 \text{ Vdc}, V_{DS} = 0, T_A = +125^{\circ}\text{C})$		I _{GSS}	_ _	_ _	-1.0 -1.0	nAdc μAdc
Gate Source Cutoff Voltage (V _{DS} = 10 Vdc, I _D = 1.0 nAdc)	J309 J310	$V_{GS(off)}$	-1.0 -2.0	- -	-4.0 -6.5	Vdc
ON CHARACTERISTICS						
Zero – Gate –Voltage Drain Current ⁽¹⁾ (V _{DS} = 10 Vdc, V _{GS} = 0)	J309 J310	I _{DSS}	12 24	- -	30 60	mAdc
Gate-Source Forward Voltage (V _{DS} = 0, I _G = 1.0 mAdc)		$V_{GS(f)}$	-	-	1.0	Vdc
SMALL-SIGNAL CHARACTERISTICS						
Common-Source Input Conductance (V _{DS} = 10 Vdc, I _D = 10 mAdc, f = 100 MHz)	J309 J310	Re(y _{is})	-	0.7 0.5	- -	mmhos
Common-Source Output Conductance (V _{DS} = 10 Vdc, I _D = 10 mAdc, f = 100 MHz)		Re(y _{os})	_	0.25	-	mmhos
Common-Gate Power Gain (V _{DS} = 10 Vdc, I _D = 10 mAdc, f = 100 MHz)		G _{pg}	_	16	-	dB
Common–Source Forward Transconductance ($V_{DS} = 10 \text{ Vdc}, I_D = 10 \text{ mAdc}, f = 100 \text{ MHz}$)		Re(y _{fs})	_	12	-	mmhos
Common-Gate Input Conductance (V _{DS} = 10 Vdc, I _D = 10 mAdc, f = 100 MHz)		Re(y _{ig})	_	12	_	mmhos
Common–Source Forward Transconductance $(V_{DS} = 10 \text{ Vdc}, I_D = 10 \text{ mAdc}, f = 1.0 \text{ kHz})$	J309 J310	9 _{fs}	10000 8000	- -	20000 18000	μmhos
Common-Source Output Conductance (V _{DS} = 10 Vdc, I _D = 10 mAdc, f = 1.0 kHz)		gos	_	-	250	μmhos
Common-Gate Forward Transconductance (V _{DS} = 10 Vdc, I _D = 10 mAdc, f = 1.0 kHz)	J309 J310	g _{fg}	-	13000 12000	- -	μmhos
Common-Gate Output Conductance (V _{DS} = 10 Vdc, I _D = 10 mAdc, f = 1.0 kHz)	J309 J310	gog	- -	100 150	- -	μmhos
Gate-Drain Capacitance (V _{DS} = 0, V _{GS} = -10 Vdc, f = 1.0 MHz)		C_{gd}	-	1.8	2.5	pF
Gate-Source Capacitance (V _{DS} = 0, V _{GS} = -10 Vdc, f = 1.0 MHz)		C _{gs}	-	4.3	5.0	pF
FUNCTIONAL CHARACTERISTICS	· · · · · · · · · · · · · · · · · · ·					
Equivalent Short–Circuit Input Noise Voltage (V _{DS} = 10 Vdc, I _D = 10 mAdc, f = 100 Hz)		e _n	_	10	_	nV/√ Hz

^{1.} Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 3.0%.

ORDERING INFORMATION

Devic	e Package	Shipping [†]
J309	TO-92	
J309G	TO-92 (Pb-Free)	1000 Units / Bulk
J310	TO-92	
J310G	TO-92 (Pb-Free)	1000 Units / Bulk
J310RLRP	TO-92	
J310RLRPG	TO-92 (Pb-Free)	2000 Units / Tape & Ammo Box
J310ZL1	TO-92	
J310ZL1G	TO-92 (Pb-Free)	2000 Units / Tape & Ammo Box

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

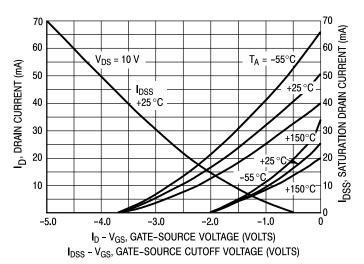


Figure 1. Drain Current and Transfer Characteristics versus Gate-Source Voltage

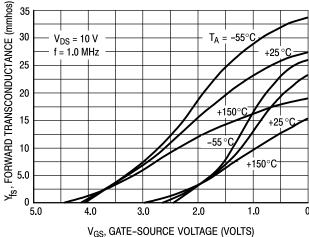


Figure 2. Forward Transconductance versus Gate-Source Voltage

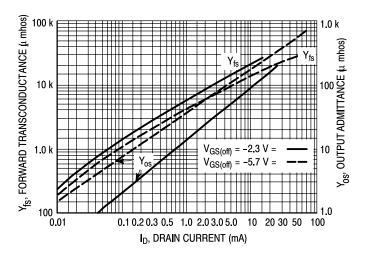


Figure 3. Common-Source Output
Admittance and Forward Transconductance
versus Drain Current

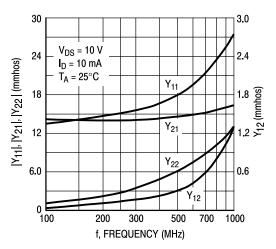


Figure 5. Common-Gate Y Parameter Magnitude versus Frequency

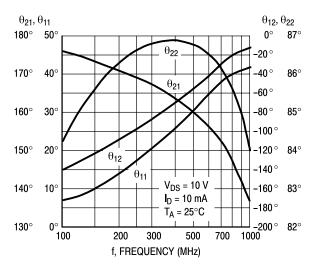


Figure 7. Common-Gate Y Parameter Phase-Angle versus Frequency

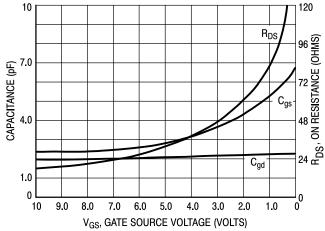


Figure 4. On Resistance and Junction Capacitance versus Gate-Source Voltage

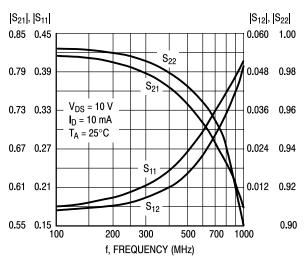


Figure 6. Common-Gate S Parameter Magnitude versus Frequency

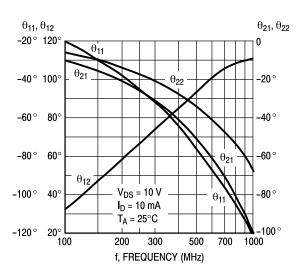
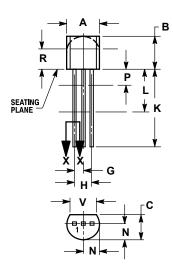
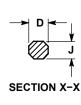


Figure 8. S Parameter Phase-Angle versus Frequency

PACKAGE DIMENSIONS

TO-92 (TO-226) CASE 29-11 ISSUE AL





NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI
 V14 5M 1982
- Y14.5M, 1982. 2. CONTROLLING DIMENSION: INCH.
- 3. CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED.
- 4. LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

	INCHES		MILLIMETERS		
DIM	MIN	MAX	MIN	MAX	
Α	0.175	0.205	4.45	5.20	
В	0.170	0.210	4.32	5.33	
С	0.125	0.165	3.18	4.19	
D	0.016	0.021	0.407	0.533	
G	0.045	0.055	1.15	1.39	
Н	0.095	0.105	2.42	2.66	
J	0.015	0.020	0.39	0.50	
K	0.500		12.70		
L	0.250		6.35		
N	0.080	0.105	2.04	2.66	
P		0.100		2.54	
R	0.115		2.93		
٧	0.135		3.43		

STYLE 5:

PIN 1. DRAIN

2. SOURCE

3. GATE

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