

August 2013

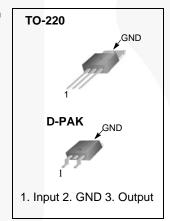
# KA78M05 / LM78M05 / MC78M05 3-Terminal 0.5 A Positive Voltage Regulator

## **Features**

- Output Current up to 0.5 A
- Output Voltages of 5 V
- Thermal Overload Protection
- Short-Circuit Protection
- Output Transistor Safe Operating Area (SOA) Protection

# **Description**

The KA78M05 / LM78M05 / MC78M05 series of threeterminal positive regulators is available in the TO-220 / D-PAK packages, making it useful in a wide range of applications.



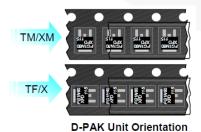
# Ordering Information(1)

Product Number	Package	Packing Method	Operating Temperature	
KA78M05TU	TO-220 (Dual Gauge)	Rail		
KA78M05RTM	D-PAK	Tape and Reel	0 to +125°C	
MC78M05CDTX	D-PAN	Tape and Reel		
LM78M05CT	TO-220 (Single Gauge)	Rail		

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#### Note:

1. Refer to below figure for TM / TF suffix of DPAK packing option.



## **Block Diagram**

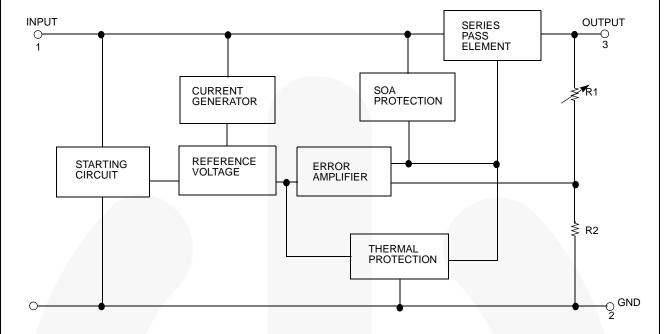


Figure 1. Block Diagram

# **Absolute Maximum Ratings**

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

Symbol	Parameter		Value	Unit	
$V_{I}$	Input Voltage (for V <sub>O</sub> = 5 V)		35	V	
$R_{\theta JC}$	Thermal Resistance, Junction-Case <sup>(2)</sup>	TO-220 (T <sub>C</sub> = +25°C)	2.5	°C/W	
R <sub>θJA</sub> Thermal	Thermal Resistance, Junction-Air <sup>(2, 3)</sup>	TO-220 (T <sub>A</sub> = +25°C)	66	°C/W	
	Thermal Resistance, Junction-All	D-PAK ( $T_A = +25^{\circ}C$ )	92		
T <sub>OPR</sub>	Operating Junction Temperature Range		0 to +125	°C	
$T_{J(MAX)}$	Maximum Junction Temperature Range		150	°C	
T <sub>STG</sub>	Storage Temperature Range		-65 to +150	°C	

#### Notes:

- Thermal resistance test board.
   Size: 76.2 mm x 114.3 mm x 1.6 mm (1S0P)
   JEDEC standard: JESD51-3, JESD51-7
- 3. Assume no ambient airflow.

# **Electrical Characteristics**

Refer to the test circuits,  $0 \le T_J \le$  +125°C,  $I_O$  = 350 mA,  $V_I$  = 10 V,  $C_I$  = 0.33  $\mu$ F,  $C_O$  = 0.1  $\mu$ F unless otherwise specified.

Symbol	Parameter	Conditions		Min.	Тур.	Max.	Unit
		$T_J = +25^{\circ}C$		4.8	5.0	5.2	V
V <sub>O</sub>	Output Voltage	I <sub>O</sub> = 5 mA to 350 mA, V <sub>I</sub> = 7 V to 20 V		4.75	5.00	5.25	
ΔV <sub>O</sub>	Line Regulation <sup>(4)</sup>	$I_{O} = 200 \text{ mA}$ $T_{J} = +25^{\circ}\text{C}$	$V_1 = 7 \text{ V to } 25 \text{ V}$			100	mV
			V <sub>I</sub> = 8 V to 25 V			50	
ΔV <sub>O</sub> Load Regulation <sup>(4)</sup>	Load Population <sup>(4)</sup>	$I_{O} = 5 \text{ mA to } 0.5 \text{ A}, T_{J} = +25^{\circ}\text{C}$				100	mV
	Load Regulation (*)	$I_{O} = 5 \text{ mA to } 20$	0 mA, $T_J = +25$ °C			50	IIIV
IQ	Quiescent Current	$T_J = +25^{\circ}C$			4.0	6.0	mA
Δl <sub>Q</sub> Quid	Quiescent Current Change	$I_0 = 5 \text{ mA to } 350$	0 mA			0.5	
		I <sub>O</sub> = 200 mA, V <sub>I</sub> = 8 V to 25 V				0.8	mA
ΔV/ΔΤ	Output Voltage Drift	I <sub>O</sub> = 5 mA T <sub>J</sub> = 0 to +125°C			-0.5		mV/°C
V <sub>N</sub>	Output Noise Voltage	f = 10 Hz to 100 kHz			40		μV/Vo
RR	Ripple Rejection	f = 120 Hz, I <sub>O</sub> = 300 mA V <sub>I</sub> = 8 V to 18 V, T <sub>J</sub> = +25 °C			80		dB
V <sub>D</sub>	Dropout Voltage	$T_J = +25^{\circ}C$ , $I_O = 500 \text{ mA}$			2		V
I <sub>SC</sub>	Short-Circuit Current	$T_J = +25^{\circ}C, V_I = 35 V$			300		mA
I <sub>PK</sub>	Peak Current	$T_J = +25^{\circ}C$			700		mA

#### Note:

4. Load and line regulation are specified at constant junction temperature. Change in  $V_0$  due to heating effects must be taken into account separately. Pulse testing with low duty is used.

# **Typical Applications**

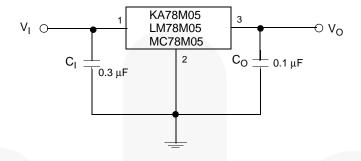


Figure 2. Fixed-Output Regulator

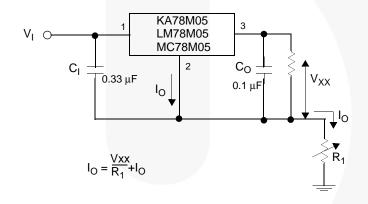


Figure 3. Constant-Current Regulator

#### Notes:

- 5. C<sub>1</sub> is required if the regulator is located an appreciable distance from the power supply filter.
- 6. Although no output capacitor is needed for stability, it does improve transient response.

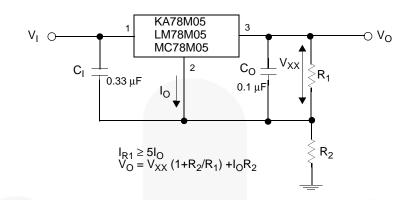


Figure 4. Circuit for Increasing Output Voltage

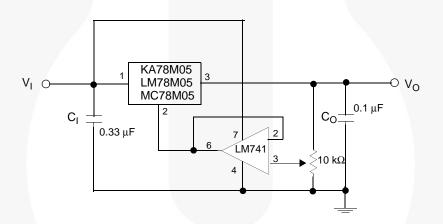


Figure 5. Adjustable Output Regulator (7 to 30 V)

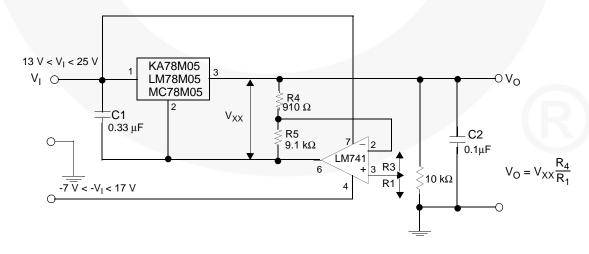


Figure 6. 0.5 to 10 V Regulator

## **Physical Dimensions**

# TO-220 (SINGLE GAUGE)

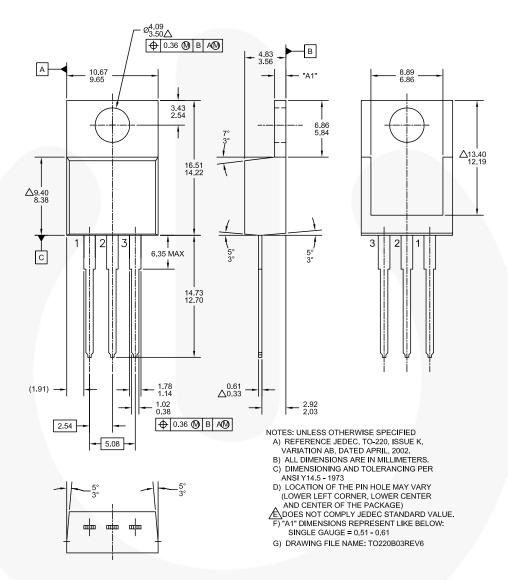


Figure 7. TO-220, MOLDED, 3-LEAD, JEDEC VARIATION AB (ACTIVE)

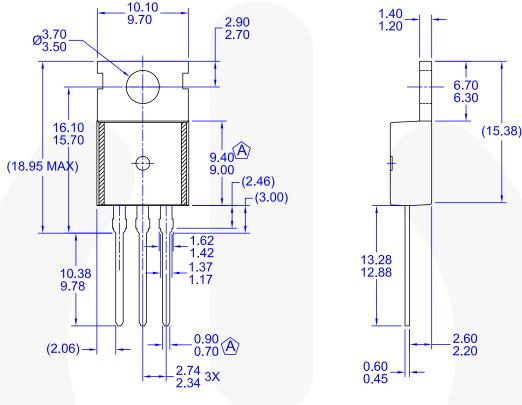
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# Physical Dimensions (Continued)

# TO-220 (DUAL GAUGE)



### NOTES:

- (A) CONFORMS TO JEDEC TO-220

  VARIATION AB EXCEPT WHERE NOTED

  B) ALL DIMENSIONS ARE IN MILLIMETERS.
  - C) DIMENSIONS ARE EXCLUSIVE OF BURRS,
  - MOLD FLASH, AND TIE BAR EXTRUSIONS.
  - D) DRAWING FILE/REVISION: MKT-TO220Y03REV1

Figure 8. TO220, MOLDED, 3-LEAD, NON-JEDEC VARIATION AB [DUAL GUAGE]

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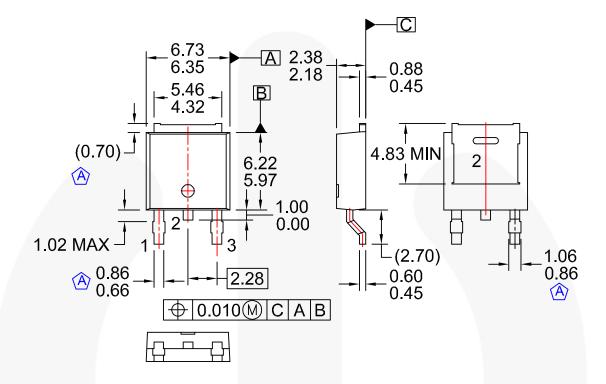
10.20

9.80

4.70 4.30

## Physical Dimensions (Continued)

# D-PAK



NOTES: UNLESS OTHERWISE SPECIFIED

- (A) CONFORMS TO JEDEC TO-252 VARIATION AB EXCEPT WHERE NOTED
- B) ALL DIMENSIONS ARE IN MILLIMETERS.
- C) DRAWING CONFORMS TO ASME Y14.5M-1994
- D) DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS.
- E) FORMERLY NAMED BD1733
- F) DRAWING FILE NAME: MKT-TO252D03REV1

Figure 9. 3-LEAD, TO-252, JEDEC TO-252 VAR. AB, SURFACE MOUNT (DPAK)

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