Switch-mode Power Rectifier

Features and Benefits

- Low Forward Voltage
- Low Power Loss / High Efficiency
- High Surge Capacity
- 175°C Operating Junction Temperature
- 20 A Total (10 A Per Diode Leg)
- These Devices are Pb-Free and are RoHS Compliant

Applications

- Power Supply Output Rectification
- Power Management
- Instrumentation

Mechanical Characteristics

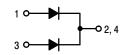
- Case: Epoxy, Molded
- Epoxy Meets UL 94, V-0 @ 0.125 in
- Weight: 1.9 Grams (Approximately)
- Finish: All External Surfaces Corrosion Resistant and Terminal Leads are Readily Solderable
- Lead Temperature for Soldering Purposes: 260°C Max. for 10 Seconds
- ESD Rating: Human Body Model = 3B Machine Model = C

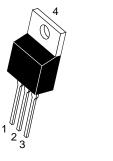


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SCHOTTKY BARRIER RECTIFIER 20 AMPERES, 45 VOLTS







TO-220 FULLPAK™ CASE 221AH

DEVICE MARKING INFORMATION

See general marking information in the device marking section on page 2 of this data sheet.

ORDERING INFORMATION

See detailed ordering and shipping information on page 3 of this data sheet.

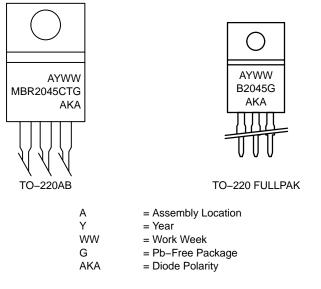


Figure 1. Marking Diagrams

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _{RWM} V _R	45	V
Average Rectified Forward Current Per Device Per Diode ($T_C = 165^{\circ}C$)	I _{F(AV)}	20 10	A
Peak Repetitive Forward Current per Diode Leg (Square Wave, 20 kHz, T _C = 163°C)	I _{FRM}	20	A
Non–Repetitive Peak Surge Current (Surge Applied at Rated Load Conditions Halfwave, Single Phase, 60 Hz)	I _{FSM}	150	A
Peak Repetitive Reverse Surge Current (2.0 μs, 1.0 kHz) See Figure 13	I _{RRM}	1.0	A
Storage Temperature Range	T _{stg}	-65 to +175	°C
Operating Junction Temperature (Note 1)	TJ	-65 to +175	°C
Voltage Rate of Change (Rated V _R)	dv/dt	10,000	V/μs

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected. 1. The heat generated must be less than the thermal conductivity from Junction–to–Ambient: $dP_D/dT_J < 1/R_{\theta JA}$.

THERMAL CHARACTERISTICS

Cha	aracteristic	Symbol	Value	Unit
Maximum Thermal Resistance (MBR2045CTG) (MBRF2045CTG)	 Junction-to-Case Junction-to-Ambient Junction-to-Case Junction-to-Ambient 	R _θ JC R _θ JA R _θ JC R _θ JA	2.0 60 4.75 75	°C/W

ELECTRICAL CHARACTERISTICS

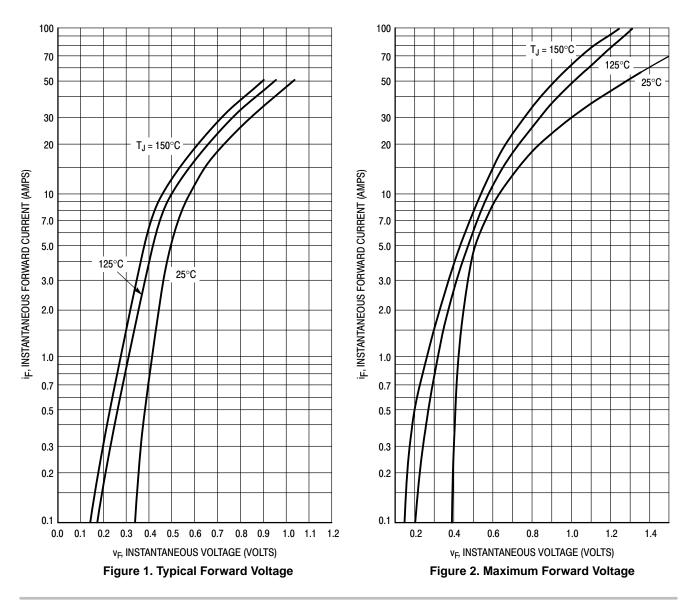
Characteristic	Symbol	Min	Тур	Max	Unit
Instantaneous Forward Voltage (Note 2) ($i_F = 10 \text{ A}, T_J = 125^{\circ}\text{C}$) ($i_F = 20 \text{ A}, T_J = 125^{\circ}\text{C}$) ($i_F = 20 \text{ A}, T_J = 25^{\circ}\text{C}$)	VF	- - -	0.50 0.67 0.71	0.57 0.72 0.84	V
Instantaneous Reverse Current (Note 2) (Rated dc Voltage, $T_J = 125^{\circ}C$) (Rated dc Voltage, $T_J = 25^{\circ}C$)	İR		10.4 0.02	15 0.1	mA

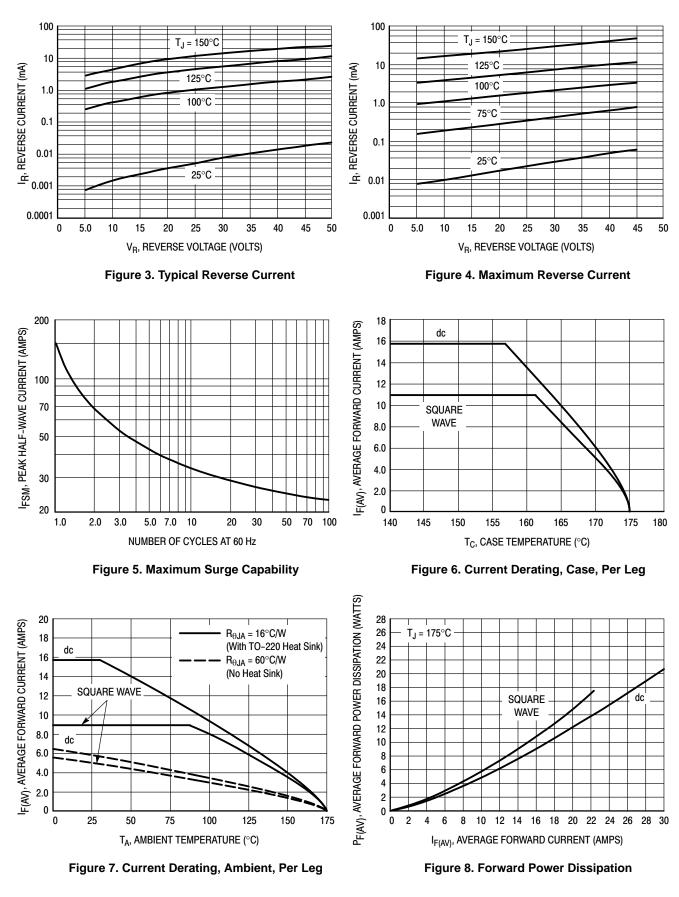
Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

2. Pulse Test: Pulse Width = 300 μ s, Duty Cycle \leq 2.0%.

ORDERING INFORMATION

Device Order Number	Package Type	Shipping [†]
MBR2045CTG	TO–220 (Pb–Free)	50 Units / Rail
MBRF2045CTG	TO-220FP (Pb-Free)	50 Units / Rail





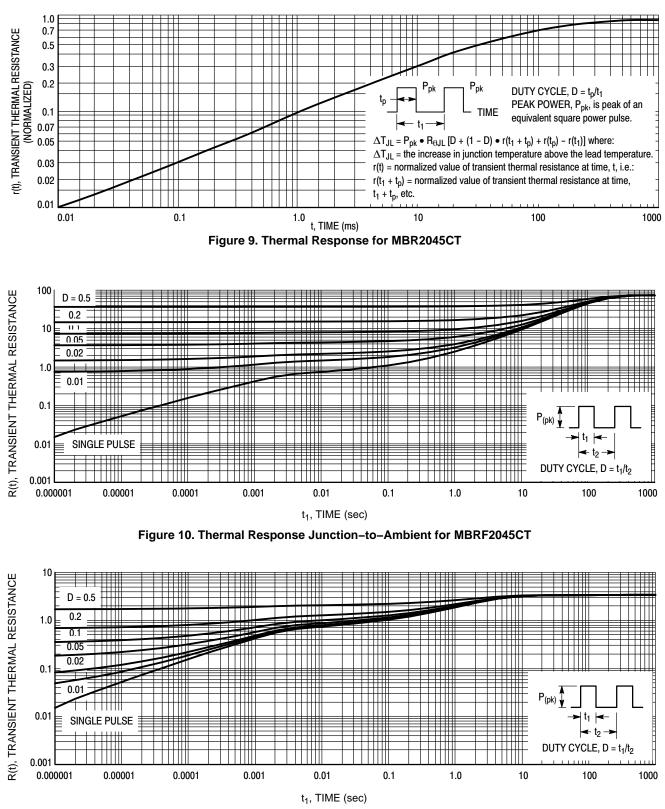


Figure 11. Thermal Response Junction-to-Case for MBRF2045CT

HIGH FREQUENCY OPERATION

Since current flow in a Schottky rectifier is the result of majority carrier conduction, it is not subject to junction diode forward and reverse recovery transients due to minority carrier injection and stored charge. Satisfactory circuit analysis work may be performed by using a model consisting of an ideal diode in parallel with a variable capacitance. (See Figure 12.)

Rectification efficiency measurements show that operation will be satisfactory up to several megahertz. For example, relative waveform rectification efficiency is approximately 70 percent at 2.0 MHz, e.g., the ratio of dc power to RMS power in the load is 0.28 at this frequency, whereas perfect rectification would yield 0.406 for sine wave inputs. However, in contrast to ordinary junction diodes, the loss in waveform efficiency is not indicative of power loss; it is simply a result of reverse current flow through the diode capacitance, which lowers the dc output voltage.

12 V

0

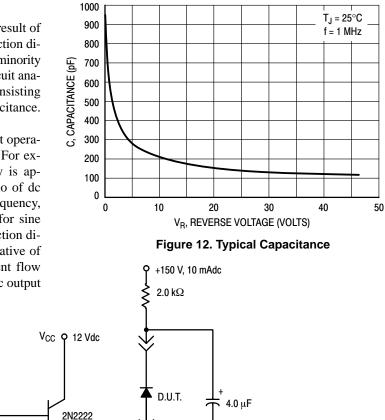
2.0 μs 1.0 kHz 100

CURRENT

AMPLITUDE

ADJUST

0-10 AMPS



2N6277

1.0 CARBON

1N5817

≶

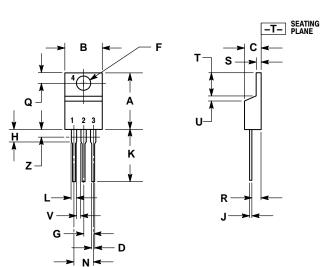
100

CARBON

Figure 13. Test Circuit for dv/dt and Reverse Surge Current

PACKAGE DIMENSIONS

TO-220 CASE 221A-09 **ISSUE AH**

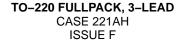


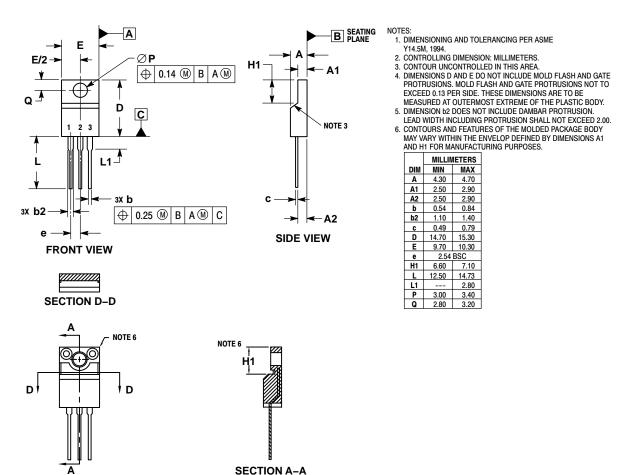
NOTES: 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. 2. CONTROLLING DIMENSION: INCH. 3. DIMENSION Z DEFINES A ZONE WHERE ALL BODY AND LEAD IRREGULARITIES ARE ALLOWED.

	INC	HES	MILLIMETER	
DIM	MIN	MAX	MIN	MAX
Α	0.570	0.620	14.48	15.75
В	0.380	0.415	9.66	10.53
С	0.160	0.190	4.07	4.83
D	0.025	0.038	0.64	0.96
F	0.142	0.161	3.61	4.09
G	0.095	0.105	2.42	2.66
н	0.110	0.161	2.80	4.10
J	0.014	0.024	0.36	0.61
κ	0.500	0.562	12.70	14.27
L	0.045	0.060	1.15	1.52
Ν	0.190	0.210	4.83	5.33
Q	0.100	0.120	2.54	3.04
R	0.080	0.110	2.04	2.79
S	0.045	0.055	1.15	1.39
Т	0.235	0.255	5.97	6.47
U	0.000	0.050	0.00	1.27
V	0.045		1.15	
Ζ		0.080		2.04

PIN 1. ANODE 2. CATHODE 3. ANODE 4. CATHODE

PACKAGE DIMENSIONS





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PUBLICATION ORDERING INFORMATION

ALTERNATE CONSTRUCTION

LITERATURE FULFILLMENT

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