TOSHIBA Bipolar Digital Integrated Circuit Silicon Monolithic

TD62783APG,TD62783AFG

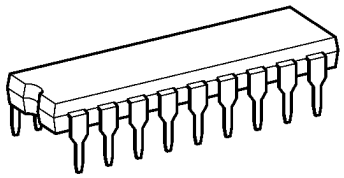
8 ch High-Voltage Source Driver

The TD62783APG/AFG Series are comprised of eight source current Transistor Array.

These drivers are specifically designed for fluorescent display applications.

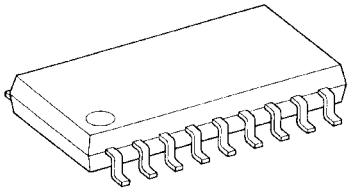
Applications include relay, hammer and lamp drivers.

TD62783APG



DIP18-P-300-2.54D

TD62783AFG



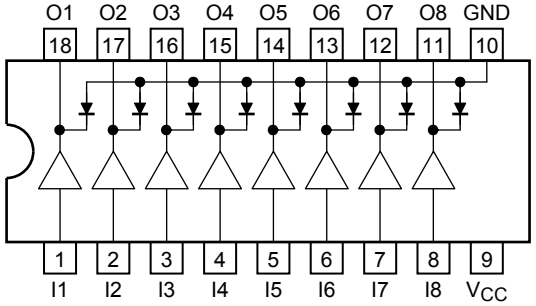
SOP18-P-375-1.27

Weight
DIP18-P-300-2.54D : 1.47 g (typ.)
SOP18-P-375-1.27 : 0.41 g (typ.)

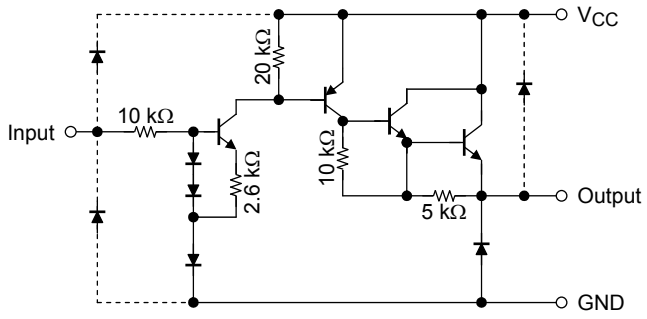
Features

- High output voltage: V_{CC} = 50 V (min)
- Output current (single output): I_{OUT} = −500 mA (max)
- Output clamp diodes
- Single supply voltage
- Input compatible with various types of logic
- Package type-APG: DIP-18 pin
- Package type-AFG: SOP-18 pin
- Designation: TTL, 5V CMOS

Pin Assignment (top view)



Schematics (each driver)



Note: The input and output parasitic diodes cannot be used as clamp diodes.

Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit
Supply voltage		V_{CC}	50	V
Output current		I_{OUT}	−500	mA/ch
Input voltage		V_{IN}	15	V
Clamp diode reverse voltage		V_R	50	V
Clamp diode forward current		I_F	500	mA
Power dissipation	APG	P_D (Note)	1.47	W
	AFG		0.96	
Operating temperature		T_{opr}	−40 to 85	°C
Storage temperature		T_{stg}	−55 to 150	°C

Note: Derated above 25°C in the proportion of 11.7 mW/°C (APG type), 7.7 mW/°C (AFG type)

Operating Ranges (Ta = −40 to 80°C)

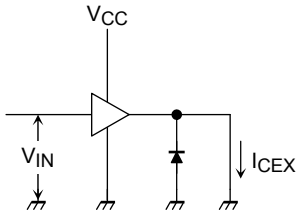
Characteristics		Symbol	Test Condition		Min	Typ.	Max	Unit
Supply voltage		V_{CC}	—		—	—	50	V
Output current		I_{OUT}	Ta = 85°C, T_j = 120°C, t_{pw} = 25 ms	Duty = 10% 8 circuits	—	—	−260	mA/ch
				Duty = 50% 8 circuits	—	—	−59	
	AFG			Duty = 10% 8 circuits	—	—	−180	
				Duty = 50% 8 circuits	—	—	−38	
Input voltage		V_{IN}	—		—	—	12	V
Input voltage	Output ON	$V_{IN\ (ON)}$	—		2.0	5.0	15	V
	Output OFF	$V_{IN\ (OFF)}$	—		0	—	0.8	
Clamp diode reverse voltage		V_R	—		—	—	50	V
Clamp diode forward current		I_F	—		—	—	400	mA
Power dissipation	APG	P_D	—		—	—	0.52	W
	AFG		—		—	—	0.35	

Electrical Characteristics (Ta = 25°C)

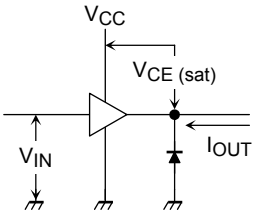
Characteristics	Symbol	Test Circuit	Test Condition	Min	Typ.	Max	Unit
Output leakage current	I_{CEX}	1	$V_{CC} = V_{CC}$ max V_{IN} = 0.4 V Ta = 25°C	—	—	100	μA
Output saturation voltage	$V_{CE\ (sat)}$	2	$V_{IN} = V_{IN\ (ON)}$, I_{OUT} = −350 mA	—	—	2.0	V
			$V_{IN} = V_{IN\ (ON)}$, I_{OUT} = −225 mA	—	—	1.9	
			$V_{IN} = V_{IN\ (ON)}$, I_{OUT} = −100 mA	—	—	1.8	
Input current	$I_{IN\ (ON)}$	3	V_{IN} = 2.4 V	—	36	52	μA
			V_{IN} = 3.85 V	—	180	260	
Input voltage	$V_{IN\ (ON)}$	4	V_{CE} = 2.0 V, I_{OUT} = −350 mA	—	—	2.0	V
	$V_{IN\ (OFF)}$		I_{OUT} = −500 μA	0.8	—	—	
Supply current	$I_{CC\ (ON)}$	3	$V_{IN} = V_{IN\ (ON)}$, V_{CC} = 50 V	—	—	2.5	mA/ch
Clamp diode reverse current	I_R	5	V_R = 50 V	—	—	50	μA
Clamp diode forward voltage	V_F	6	I_F = 350 mA	—	—	2.0	V
Turn-ON delay	t_{ON}	7	$V_{CC} = V_{CC}$ max R_L = 125 Ω C_L = 15 pF	—	0.15	—	μs
Turn-OFF delay	t_{OFF}	7	$V_{CC} = V_{CC}$ max R_L = 125 Ω C_L = 15 pF	—	1.8	—	μs

Test Circuit

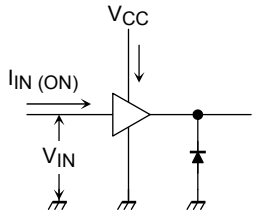
1. I_{CEX}



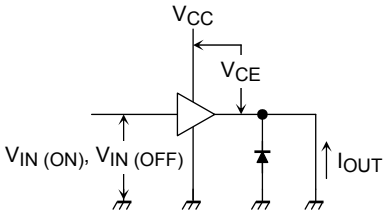
2. $V_{CE\ (sat)}$



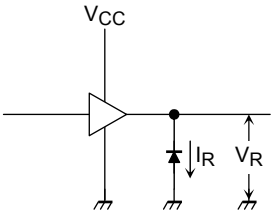
3. $I_{IN\ (ON)}$, I_{CC}



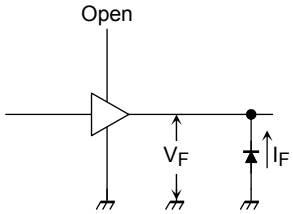
4. $V_{IN\ (ON)}$, $V_{IN\ (OFF)}$



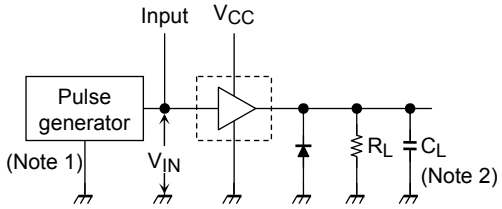
5. I_R

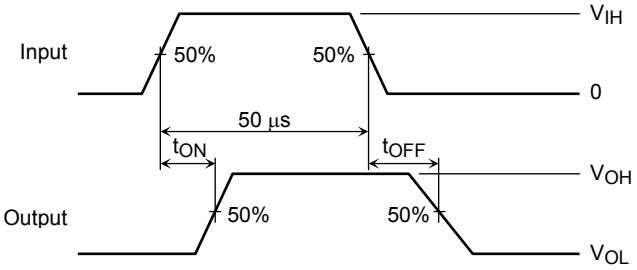


6. V_F



7. t_{ON}, t_{OFF}





Note 1: Pulse Width 50 μs, Duty Cycle 10%
Output Impedance 50 Ω, $t_r \leq 5$ ns, $t_f \leq 10$ ns

Note 2: C_L includes probe and jig capacitance.

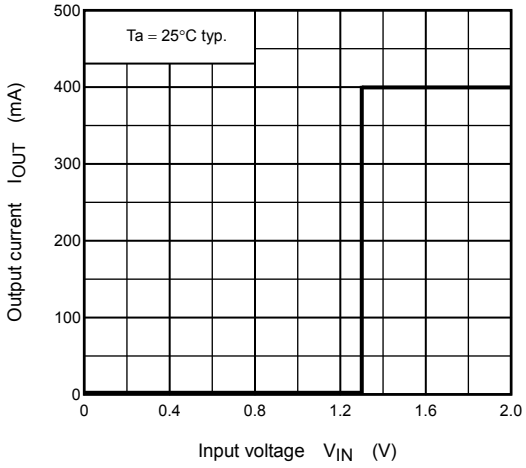
Precautions for Using

This IC does not integrate protection circuits such as overcurrent and overvoltage protectors.

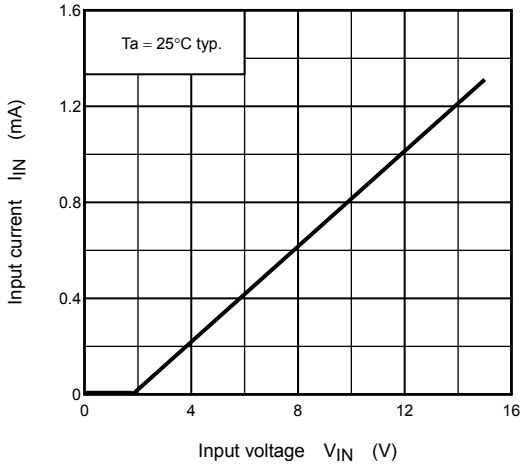
Thus, if excess current or voltage is applied to the IC, the IC may be damaged. Please design the IC so that excess current or voltage will not be applied to the IC.

Utmost care is necessary in the design of the output line, V_{CC} and GND line since IC may be destroyed due to short-circuit between outputs, air contamination fault, or fault by improper grounding.

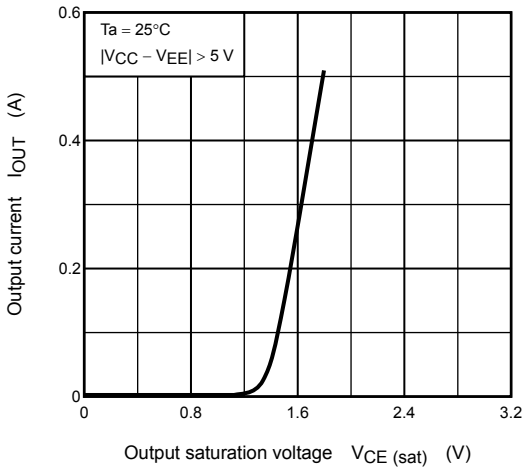
$I_{OUT} – V_{IN}$



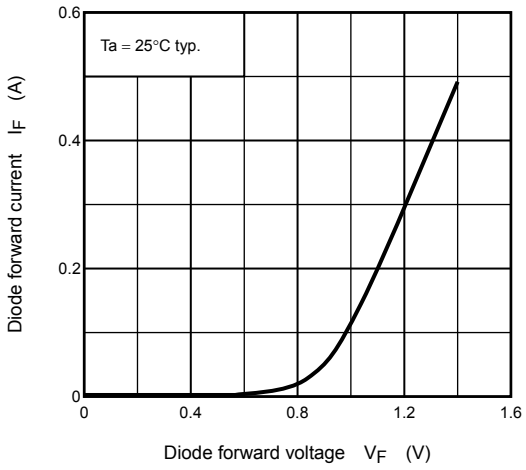
$I_{IN} – V_{IN}$



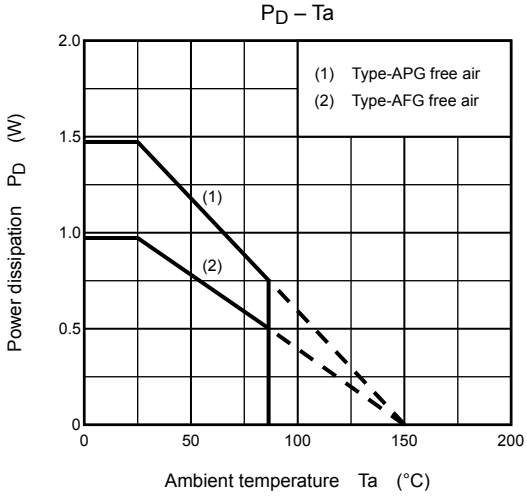
$I_{OUT} – V_{CE\ (sat)}$



$I_F – V_F$

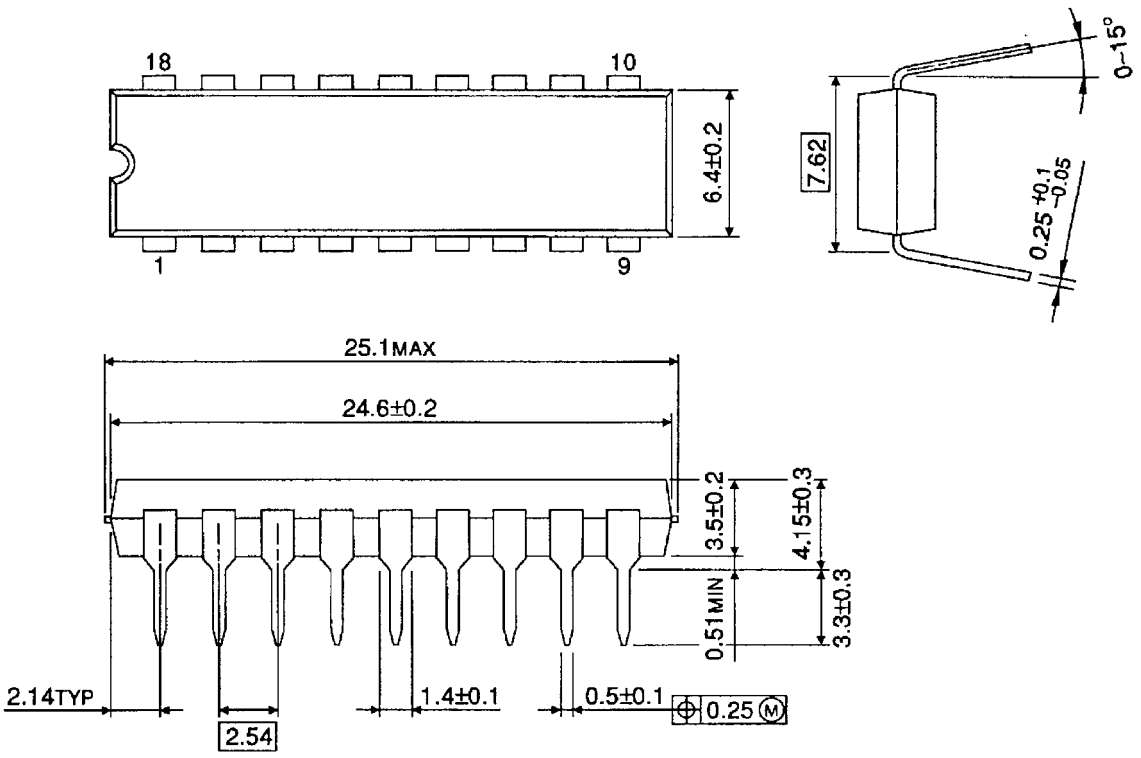


$P_D – Ta$



Package Dimensions

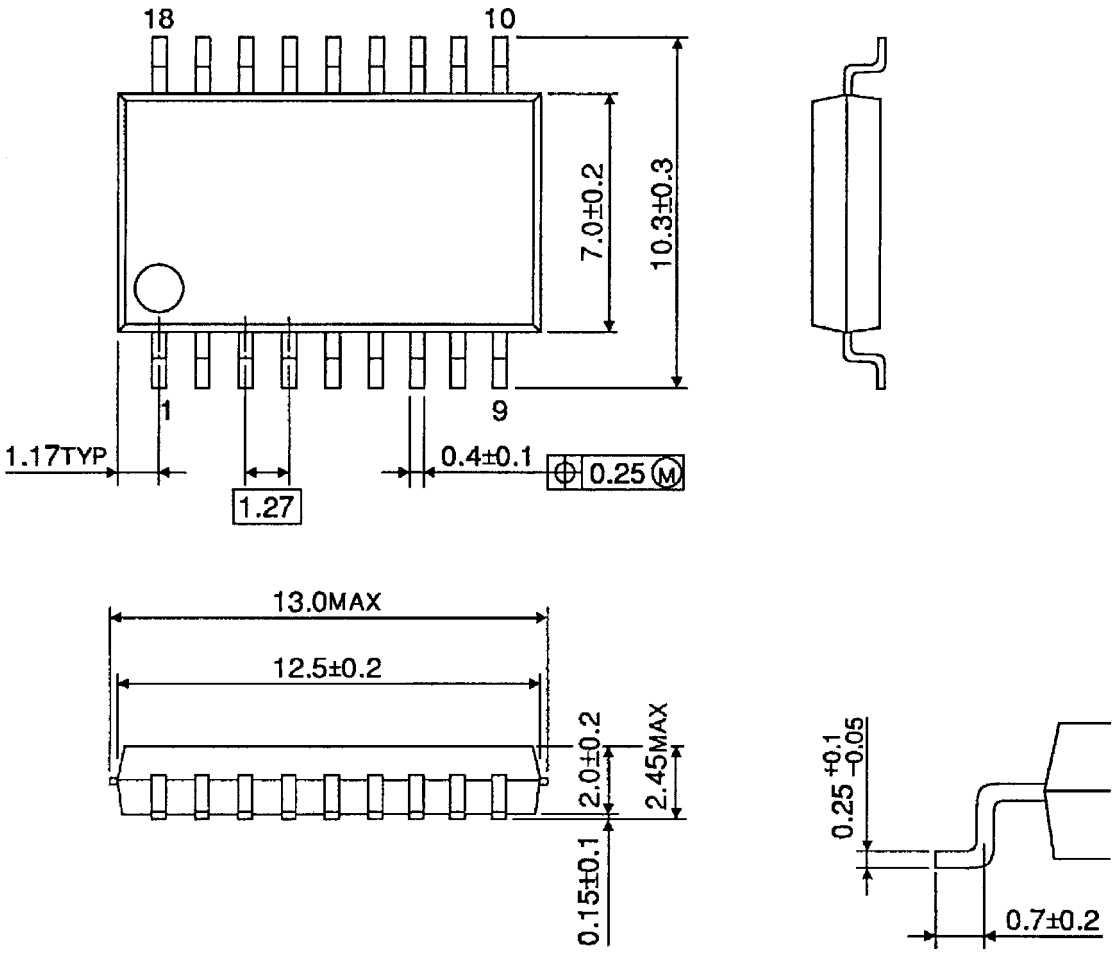
DIP18-P-300-2.54D Unit : mm



Weight: 1.47 g (typ.)

Package Dimensions

SOP18-P-375-1.27 Unit : mm



Weight: 0.41 g (typ.)

Notes on Contents

1. Equivalent Circuits

The equivalent circuit diagrams may be simplified or some parts of them may be omitted for explanatory purposes.

2. Test Circuits

Components in the test circuits are used only to obtain and confirm the device characteristics. These components and circuits are not guaranteed to prevent malfunction or failure from occurring in the application equipment.

IC Usage Considerations

Notes on Handling of ICs

(1) The absolute maximum ratings of a semiconductor device are a set of ratings that must not be exceeded, even for a moment. Do not exceed any of these ratings.
Exceeding the rating(s) may cause the device breakdown, damage or deterioration, and may result injury by explosion or combustion.

(2) Use an appropriate power supply fuse to ensure that a large current does not continuously flow in case of over current and/or IC failure. The IC will fully break down when used under conditions that exceed its absolute maximum ratings, when the wiring is routed improperly or when an abnormal pulse noise occurs from the wiring or load, causing a large current to continuously flow and the breakdown can lead smoke or ignition. To minimize the effects of the flow of a large current in case of breakdown, appropriate settings, such as fuse capacity, fusing time and insertion circuit location, are required.

(3) If your design includes an inductive load such as a motor coil, incorporate a protection circuit into the design to prevent device malfunction or breakdown caused by the current resulting from the inrush current at power ON or the negative current resulting from the back electromotive force at power OFF. IC breakdown may cause injury, smoke or ignition.
Use a stable power supply with ICs with built-in protection functions. If the power supply is unstable, the protection function may not operate, causing IC breakdown. IC breakdown may cause injury, smoke or ignition.

(4) Do not insert devices in the wrong orientation or incorrectly.
Make sure that the positive and negative terminals of power supplies are connected properly. Otherwise, the current or power consumption may exceed the absolute maximum rating, and exceeding the rating(s) may cause the device breakdown, damage or deterioration, and may result injury by explosion or combustion.
In addition, do not use any device that is applied the current with inserting in the wrong orientation or incorrectly even just one time.

(5) Carefully select external components (such as inputs and negative feedback capacitors) and load components (such as speakers), for example, power amp and regulator.
If there is a large amount of leakage current such as input or negative feedback condenser, the IC output DC voltage will increase. If this output voltage is connected to a speaker with low input withstand voltage, overcurrent or IC failure can cause smoke or ignition. (The over current can cause smoke or ignition from the IC itself.) In particular, please pay attention when using a Bridge Tied Load (BTL) connection type IC that inputs output DC voltage to a speaker directly.

Points to Remember on Handling of ICs

(1) Heat Radiation Design
In using an IC with large current flow such as power amp, regulator or driver, please design the device so that heat is appropriately radiated, not to exceed the specified junction temperature (Tj) at any time and condition. These ICs generate heat even during normal use. An inadequate IC heat radiation design can lead to decrease in IC life, deterioration of IC characteristics or IC breakdown. In addition, please design the device taking into considerate the effect of IC heat radiation with peripheral components.

(2) Back-EMF
When a motor rotates in the reverse direction, stops or slows down abruptly, a current flow back to the motor's power supply due to the effect of back-EMF. If the current sink capability of the power supply is small, the device's motor power supply and output pins might be exposed to conditions beyond absolute maximum ratings. To avoid this problem, take the effect of back-EMF into consideration in system design.

About solderability, following conditions were confirmed

- Solderability

(1) Use of Sn-37Pb solder Bath
- solder bath temperature = 230°C
- dipping time = 5 seconds
- the number of times = once
- use of R-type flux

(2) Use of Sn-3.0Ag-0.5Cu solder Bath
- solder bath temperature = 245°C
- dipping time = 5 seconds
- the number of times = once
- use of R-type flux

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